

Listing of the Claims:

This listing of claims will replace all prior versions and listings of claims in the application.

1. (Cancelled)

2. (Currently Amended) A TFT LCD (thin film transistor liquid crystal display) comprising:

a first substrate and a second substrate;

a scanning line on the first substrate;

a signal line formed to cross the scanning line, wherein the signal line does not include an extension pattern;

figs. 2A, 2B a channel layer formed along the signal line and extended to a portion of the scanning line;

source and drain electrodes formed separated on the channel layer over the scanning line;

a pixel electrode connected to the drain electrode; and

a liquid crystal layer formed between the first substrate and the second substrate;

wherein the drain electrode is parallel to the signal line and is formed to cross the scanning line.

3. (Previously Presented) A TFT LCD as claimed in claim 2, wherein the channel ^{a width} layer has a width smaller than a width of the signal line and the scanning line.

4. (Previously Presented) A TFT LCD as claimed in claim 2, further comprising a gate insulating layer between the scanning line and the channel layer.

5. (Previously Presented) A TFT LCD as claimed in claim 2, further comprising an ohmic contact layer between the source and drain electrodes and the channel layer.
each of

6. (Previously Presented) A TFT LCD as claimed in claim 2, wherein the source electrode and the signal line are formed as a unit.

7. (Cancelled)

8. (Cancelled)

9. (Currently Amended) A TFT LCD comprising:
a first substrate and a second substrate;
a plurality of scanning lines on the first substrate;
figs. 2A, 2B a gate insulating layer on an entire surface inclusive of the scanning lines;
a channel layer on the gate insulating layer to cross the scanning lines having a portion extended to a top of at least one of the plurality of scanning lines;
source and drain electrodes formed separated on the channel layer over the scanning lines;
a signal line formed as a unit with the source electrode along the channel layer which is formed to cross the scanning lines, wherein the signal line does not include an extension pattern;

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- 11 a protection film formed on an entire surface inclusive of the signal line;
 14 a pixel electrode connected to the drain electrode on the protection film; and[[,]]
 a liquid crystal layer formed between the first substrate and the second substrate[[,]],
 14 wherein the drain electrode is parallel to the signal line and is formed to cross the
 15 scanning line.

10. (Cancelled)

11. (Previously Presented) A TFT LCD as claimed in claim 9, wherein the channel
 2 layer has a width smaller than a width of the signal line and the scanning line.

12. (Previously Presented) A TFT LCD as claimed in claim 9, further comprising an
 2 ohmic contact layer between the source and drain electrodes and the channel layer.

13. (Previously Presented) A TFT LCD as claimed in claim 9, wherein the scanning
 1 line has a portion enlarged in the vicinity of the signal line.

14. (Original) A TFT LCD as claimed in claim 13, wherein the channel
 2 layer is formed along the signal line over the scanning line, and has a width enlarged as much as
 3 a width of the scanning line is enlarged.

15. (Currently Amended) A TFT LCD having a first substrate, a second substrate, and liquid crystal sealed between the first and second substrates, comprising:

a scanning line on the first substrate;

a gate insulating layer on the scanning line;

a channel layer on the gate insulating layer;

a signal line formed to cross the scanning line to cover a portion of the channel layer,

wherein the signal line does not include an extension pattern;

a drain electrode formed on the channel layer spaced a distance away from the signal line in parallel to the signal line;

a protection film formed on an entire surface of the first substrate inclusive of the drain electrode; and

a pixel electrode formed on the protection film connected to the drain electrode;

wherein the drain electrode is parallel to the signal line ^{is} and formed to cross the scanning

line.

16. (Original) A TFT LCD as claimed in claim 15, wherein the channel layer is formed along the signal line.

17. (Original) A TFT LCD as claimed in claim 16, wherein the channel layer has a width smaller than a width of the signal line ^{a width of} and the scanning line.

18. (Original) A TFT LCD as claimed in claim 15, wherein the signal line serves as a source electrode disposed opposite to the drain electrode.

19. (Original) A TFT LCD as claimed in claim 15, further comprising a gate insulating layer between the scanning line and the channel layer.

20. (Original) A TFT LCD as claimed in claim 18, further comprising an ohmic contact layer between ^{each of} the source and drain electrodes and the channel layer.